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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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|----------------------|---------------------------|---------------------|-----------------|--------------------|--------------------|
| APPL NUM 10083163 | FILING DATE 02/27/2002 | CLASS 357 438 | SUBCLASS 377 | GA 2826 2842 | EXAMINER Dickey |
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**APPLICANTS: Hokazono Akira; *TH*

**CONTINUING DATA VERIFIED:

None TH

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-392569 12/25/2001 *TH*

| | | |
|---|---|----------------------------------|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>TH 2/28/02</i> | | ATTORNEY DOCKET NO 220110US2S |
| TITLE : Semiconductor device having active regions connected together by interconnect layer and method of manufacture thereof | | |

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|---|-----------|--|----------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| Assistant Examiner | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. |
| | | Print Fig. | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Primary Examiner PREPARED FOR ISSUE Application Examiner | |
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